Department of Electronics and Communication Engineering

R&D Facilities

Sl. No.	Name of the Laboratory	Facilities	Utilization
1.	R&D Lab	HFSS-5 license	Five project batches and one faculty is utilized this facility
		Vector network analyzer	Four antennae tested using VNA and implemented 5 students projects.
		Cadence -20 license	Two project batches, three faculties are utilized this facility
		MATLAB V.15b	Eight project batches, five faculties are utilized this facility.
		MSP430 kits	One project batch utilizing this facility
		FPGA Spartan-6	One Project batch utilizing this facility
		Arm Processor	one project batch utilized this facility
		Vivado Software	One project batch and one faculty is used
		Kintex boards	One project batch and one faculty is used
		Labview	Two project batches and one faculty are utilized this facility
		All in one Bio Sensing R&D Bundle	Final year, One project batch and one faculty is used.
		Work station	Used by Internal Ph.D Research Scholars

Sl. No.	Facility Name	Details	Reason(s)for creating facility	Utilization	Areas in which students are expected to have enhanced learning experience	Relevance to POs/PSOs
1.	Labview	To Demonstrate signal processing experiment	Used as teaching tool to demonstrat esignal processing	Students of second and second year	Signal processing	PO 2,4,5 PSO2
2.	Vector network analyzer (VNA)	VNA is a test instrument that measures the response of a network as vector: real & imaginary parameters so that its performance can be characterized.	Testing of antenna characterist ics	Research scholar and students	Antenna	PO4
3.	HFSS-5 license	To analyze and design various types of Antenna	Used as teachin gtool	Students of third year	Antenna design	PO 4,5 PSO2
4.	Wicom-T	To Demonstrate Communication experiment	Used as teaching tool to demonstrat e modulation techniques	Students of third year	Signal processing and communication	PO 4,5 PSO2
5.	FPGA Spartan -6	VHDL and Verilog codes are developed to verify the functionality of the system	Used as teaching tool to demonstrat eworking of the systems	Students of second year	HDL/VLSI	PO 2,3,4,5 PSO1

6.	Advanced MSP kits	C and Assembly codes are developed to verify the functionality	Used as teaching tool to demonstrat eworking of the systems	Students of second year	Embedded Systems	PO1,2,3, 4.5 PSO1
		of the system				
7.	Intel Galileo, Arm Processor	To design and develop the specific applications	Used for designing small systems	Students of third and final year	Embedded Systems	PO1,2,3, 4.5 PSO1
8.	Vivado Software	To verify the functionality of the system developed by VHDL and Verilog codes	Used as teaching tool to demonstrat e working of the systems	Final year project students	VLSI	PO1,2,3, 4.5 PSO1
9.	Zibo boards	To verify the functionality of the system developed by VHDL and Verilog codes	Used as teaching tool to demonstrat e working of the systems	Students of second and third year	VLSI	PO1,2,3, 4.5 PSO1
10.	Nexys boards	To verify the functionality of the system developed by VHDL and Verilog codes	Used as teaching tool to demonstrat e working of the systems	Students of second and third year	VLSI	PO1,2,3, 4.5 PSO1

11.	Kintex boards	To verify the functionality of the system developed by VHDL and Verilog codes	Used as teaching tool to demonstrate working of the systems	Students of second and third year	VLSI	PO1,2,3, 4.5 PSO1
12	All in one Bio Sensing R&D Bundle from Open BCI	Cyton Daisy Biosensing Boards (16-Channels) DeFa with other Bio sensors	Research Scholars and Students	Final year, One project batch and one faculty is used.	Bio Medical Signal Processing	PO 5,11,12 PSO3
13.	Work station	Used for Digital/ Image and Signal Processing Applications	For Ph.D scholars to carry out the Research work	Used by Internal Ph.D Research Scholars	Signal Processing	PO 5, 9,11,12 PSO1